

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

# 9



Patentees: Alex Kalnitsky  
Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION  
TECHNIQUE FOR AN  
INTEGRATED CIRCUIT

Issued: November 16, 1999

Atty Dk No.: 93-C-023C3

Reissue Application

Applicants: Alex Kalnitsky  
Yih-Shung Lin

Serial No.: 09/998,595

Title: ENHANCED PLANARIZATION  
TECHNIQUE FOR AN  
INTEGRATED CIRCUIT

Filing Date: November 16, 2001

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**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that this correspondence is being deposited in the United States Postal Service as First Class Mail in an envelope addressed to: MS PETITION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, Attn: E. Shirene Willis, on this 15<sup>th</sup> day of December, 2003.

  
Signature

**REQUEST FOR EXTENSION OF TIME**

December 15, 2003

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Applicant respectfully requests that the shortened statutory period for response to the outstanding Decision Refusing Status Under 37 CFR 1.47(a) mailed August 13, 2003,

now set to expire on October 13, 2003 ~~is~~ extended two (2) months to expire on December 15, 2003.

Enclosed is our check no. 21431 in the amount of \$420.00 (2 months).

Please charge any additional fees or credit overpayment to Deposit Account No. 07-1897.

Respectfully submitted,

GRAYBEAL JACKSON HALEY LLP



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